

Listing of Claims:

1. (Currently Amended) An electronic circuit arrangement[[,]] ~~in particular an LED circuit arrangement (1),~~ having a lead (3), via which electronic circuit elements (6) of the circuit arrangement[[,]] ~~in particular~~ comprising LED components[[,]] ~~can be driven~~ are drivable by a drive circuit (2; 20; 21; 22),

wherein

the lead (3) has a plurality of coding conductors (3c, 3d), which carry a code by means of a combination of electrically interrupted and electrically continuous coding conductors (3c, 3d), said code giving an indication of specific properties of the circuit arrangement; and

wherein

said code is detectable by an evaluation circuit which is integrated in the circuit arrangement.

2. (Currently Amended) The electronic circuit arrangement as claimed in claim 1,

wherein

the ~~code can be detected by means of an~~ evaluation circuit (70; 71; 72; 73)[[,]] ~~which~~ passes a corresponding control signal to the drive circuit (2; 20; 21).

3. (Original) The electronic circuit arrangement as claimed in claim 1,

wherein

an interrupted coding conductor represents the logic state “0” and a non-interrupted coding conductor represents the logic state “1”.

4. (Currently Amended) The electronic circuit arrangement as claimed in claim 2,

wherein

at least two coding conductors (3d) ~~can in each case be~~ are individually ~~connected~~ connectable to a measurement voltage source of the drive circuit (21) and the coding conductors (3d) ~~can furthermore be connected~~ are furthermore connectable to the evaluation circuit (73).

5. (Currently Amended) The electronic circuit arrangement as claimed in claim 1 [[2]],

wherein

the evaluation circuit (72; 73) is a digital/analog converter (D/A).

6. (Original) The electronic circuit arrangement as claimed in claim 5,

wherein

the digital/analog converter (D/A) contains a resistor network.

7. (Original) The electronic circuit arrangement as claimed in claim 6,

wherein

a reference voltage (U_{ref}) of the digital/analog converter (D/A) is a measurement voltage provided by the measurement voltage source.

8. (Currently Amended) The electronic circuit arrangement as claimed in claim 1,

wherein

an electrical supply line for the circuit elements is ~~can be~~ provided by at least one electrically continuous coding conductor (3d).

9. (Currently Amended) The electronic circuit arrangement as claimed in claim 1,

wherein

the lead and the circuit arrangement are arranged on a common carrier~~[[,]] in particular on a common printed circuit board.~~

10. (Original) The electronic circuit arrangement as claimed in claim 1,

wherein

the lead is arranged on a flexible part of a carrier.

11. (Currently Amended) The electronic circuit arrangement as claimed in claim 1,

wherein

the coding conductors (3c, 3d) ~~can be interrupted~~ are interruptable by perforation, stamping and/or milling ~~or in a comparable manner.~~

12. (Currently amended) The electronic circuit arrangement as claimed in claim 1,

wherein

the lead ~~can be~~ is electrically ~~connected~~ connectable to the drive circuit and/or to the circuit arrangement (1) by plug connectors.

13. (Original) The electronic circuit arrangement as claimed in claim 1,

wherein

the circuit arrangement (1) is an LED circuit arrangement (1).

14. (Original) The electronic circuit arrangement as claimed in claim 13,

wherein

the LED circuit arrangement (1) has a plurality of LED chains each having a plurality of LED components (6), said LED chains being electrically connected in parallel with one another.

15. (Original) The electronic circuit arrangement as claimed in claim 14,

wherein

the coding is correlated by the brightness grouping of the LED components used in the LED circuit arrangement.

16. (Currently Amended) A method for coding an electronic circuit arrangement[[,]] ~~in particular an LED circuit arrangement~~, as claimed in claim 1,

wherein

the lead is coded by perforation, stamping and/or milling ~~or in a comparable manner~~ after the completion of the electronic circuit arrangement, in accordance with the properties, parameters and/or functions of said electronic circuit arrangement.

17. (New) The electronic circuit arrangement as claimed in claim 9,

wherein

the common carrier comprises a printed circuit board.

18. (New) The method for coding an electronic circuit arrangement as claimed in claim 16,

wherein

the electronic circuit arrangement comprises an LED circuit arrangement.